

PHOTOMASK

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Novel EUV mask absorber evaluation in support of next-generation EUV imaging

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ABSTRACT

In next-generation EUV imaging for foundry N5 dimensions and beyond, inherent pitch- and orientation-dependent effects on wafer level will consume a significant part of the lithography budget using the current Ta-based mask. Mask absorber optimization can mitigate these so-called mask 3D effects. Thin metal absorbers like Ni and Co have been experimentally investigated due to their high EUV absorption, but they pose challenges on the current technology of subtractive mask patterning^[1]. A simulation study of attenuated EUV phase shift masks has identified through multi-objective optimization superior imaging solutions for specific use cases and illumination conditions^[2].

Evaluating novel EUV mask absorbers evolves on two levels, demonstrating (1) improvements from lithographic perspective and (2) compatibility with the full mask supply chain including material deposition, absorber patterning, scanner environment compatibility and mask lifetime.

On the lithographic level, we have identified regions based on the material optical properties and their gain in imaging performance compared to the reference Ta-based absorber. Within each improvement region we engineered mask absorber materials to achieve both the required imaging capabilities, as well as

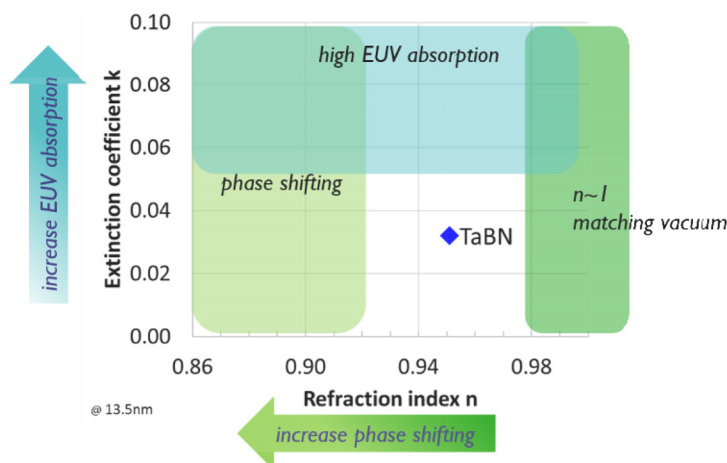


Figure 1. EUV n&k space with TaBN as the diamond and three possible n&k regions for novel absorber materials: high EUV absorption, phase matching to vacuum and intentional phase shifting.

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EDITORIAL

BACUS Photomask: Two Conferences in One Location

Artur Balasinski, Cypress Semiconductor

September 2019 edition of BACUS Photomask Symposium, collocated with the Extreme Ultraviolet Lithography Conference, highlighted the ongoing trends: convergence and divergence. This reminds me of my early encounters with the industry, when my first VP taught me: first drill down, then drill wide. I guess this adage is as good in the carpentry as it is in the semiconductor business.

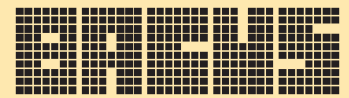
We are converging on the notion that Moore's law is alive and well. It seems like the critical mass of complex applications and the key enabler, EUV, show us that there is still money to make even at the 5 nm node. At this time, it remains a trait for the few selected heavyweights, but when design methodologies catch on, we can keep drilling down with it. The roadmap and the tasks seem to be defined, just as they have always been when following Moore's law. We only need a handful of technical breakthroughs per year to improve the process. Life is good.

What are we diverging about is how to go wide. There are still many issues relating to any technology node. But we are drilling wide with the poor sense of direction dictated by the whims of the market. This is no longer maskmaker's holiday and there are issues with pattern placement, curvilinear shapes, write time, and alignment marks, worth doing technical research. But there does not seem to be enough focus on how to make a Bay-Area-Chrome-User happy with maskshop performance. There are discussions about applications, like flat panel displays or image sensors, but does not seem to be enough about cost reduction, simplicity, or shorter leadtimes. IC Companies which decide to explore the many possibilities in the power electronics or automotive emerging markets, which are not-so-demanding on the process node, have to still put up with the old-style support.

We have new fabs, new technologies, new applications. What is missing: new companies. What kind? The enabling kind.

Thinking now of my favorite comparison with the airline industry, it is not the equipment speed nor comfort of the travel that is making a lot of difference. It is the fact that one can buy a ticket online in 1 minute, and the price differences between the carries are minor, even with all the deregulation. We are missing the sort of reticle Expedia or Travelocity, where one can turn in a mask order today and get the plate back in 3 days at the doorsteps, courtesy Amazon. This is what the divergence demands.

Are there opportunities for research in this domain? Yes. While the travel, from the user perspective, can be defined by a handful of parameters, building a mask requires hundreds of them. Such research can become the backbone of the otherwise very valuable and important technical papers in the Photomask Technology (non-EUV) mainstream. That would enhance the bifurcation of BACUS and let the users explore the best of both approaches: drilling down and drilling wide.



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Table 1. (a) Illumination settings at 0.33NA, (b) feature types through pitch, and (c) mask absorber $n&k$ used in the rigorous imaging simulations.

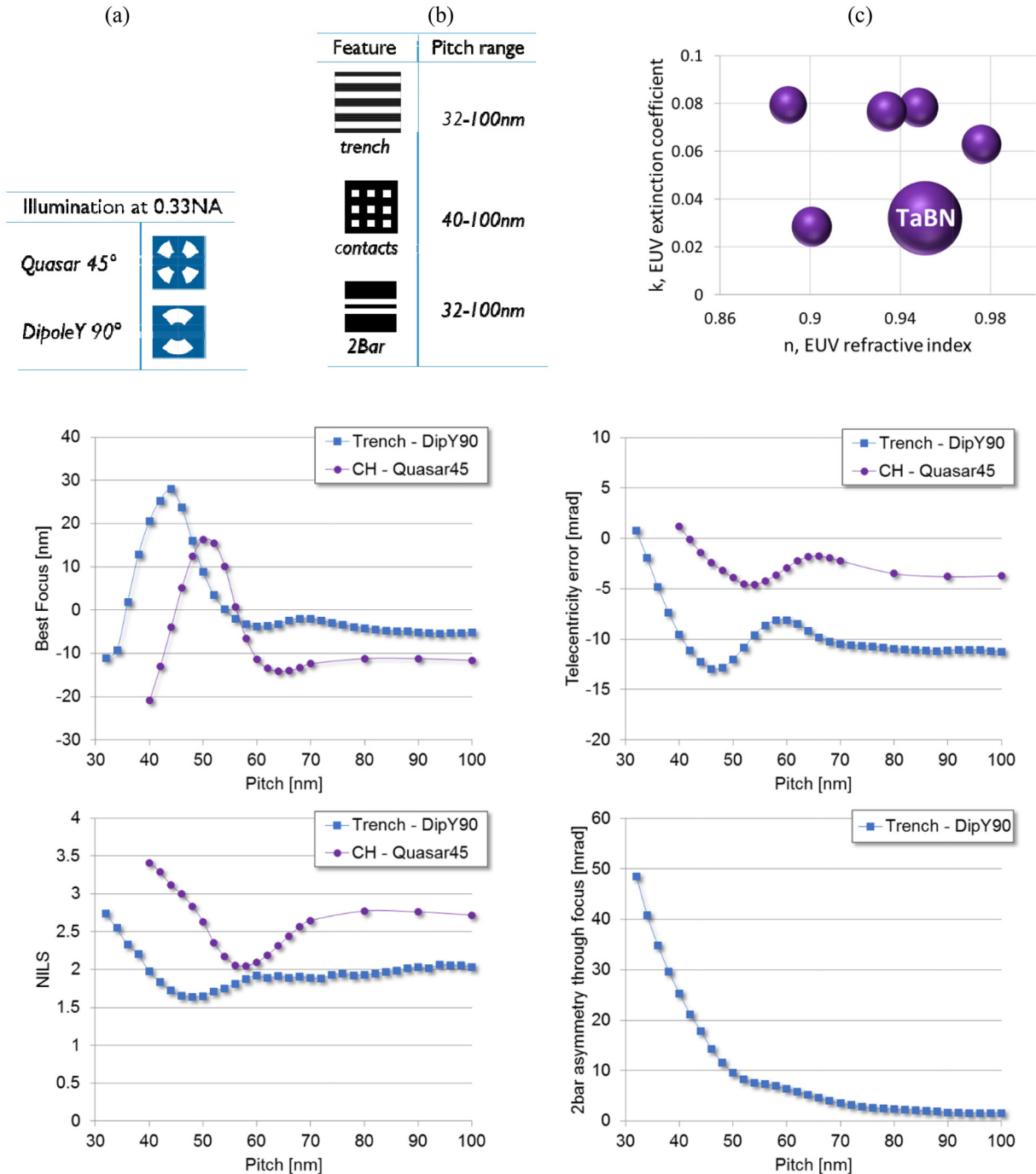


Figure 2. Mask 3D sensitive imaging metrics using the 60nm TaBN reference mask for features and illumination at NA0.33 as indicated by the legend in each plot. Left-top: best focus variation through pitch; Right-top: Telecentricity error; Left-bottom: NILS; Right-bottom: Two bar CD asymmetry through focus.

the technical requirements for an EUV mask absorber. We discuss the material development of Te-based alloys and Ag-based layered structures, because of their high EUV extinction. For the attenuated phase shift materials, we start from a Ru-base material, due to its low refractive index, and construct Ru-alloys.

On the experimental level, we examined our novel mask absorber materials against an initial mask absorber requirement list using an ex-

perimental test flow. Candidate materials are evaluated on film morphology and stability through thermal, hydrogen, EUV loading, and chemical cleaning, for their EUV optical constants by EUV reflectometry, as well as preliminary for selective dry etch.

The careful mask absorber evaluation, combining imaging simulations and experimental material tests, allowed us to narrow down to promising combinations for novel EUV mask absorbers.

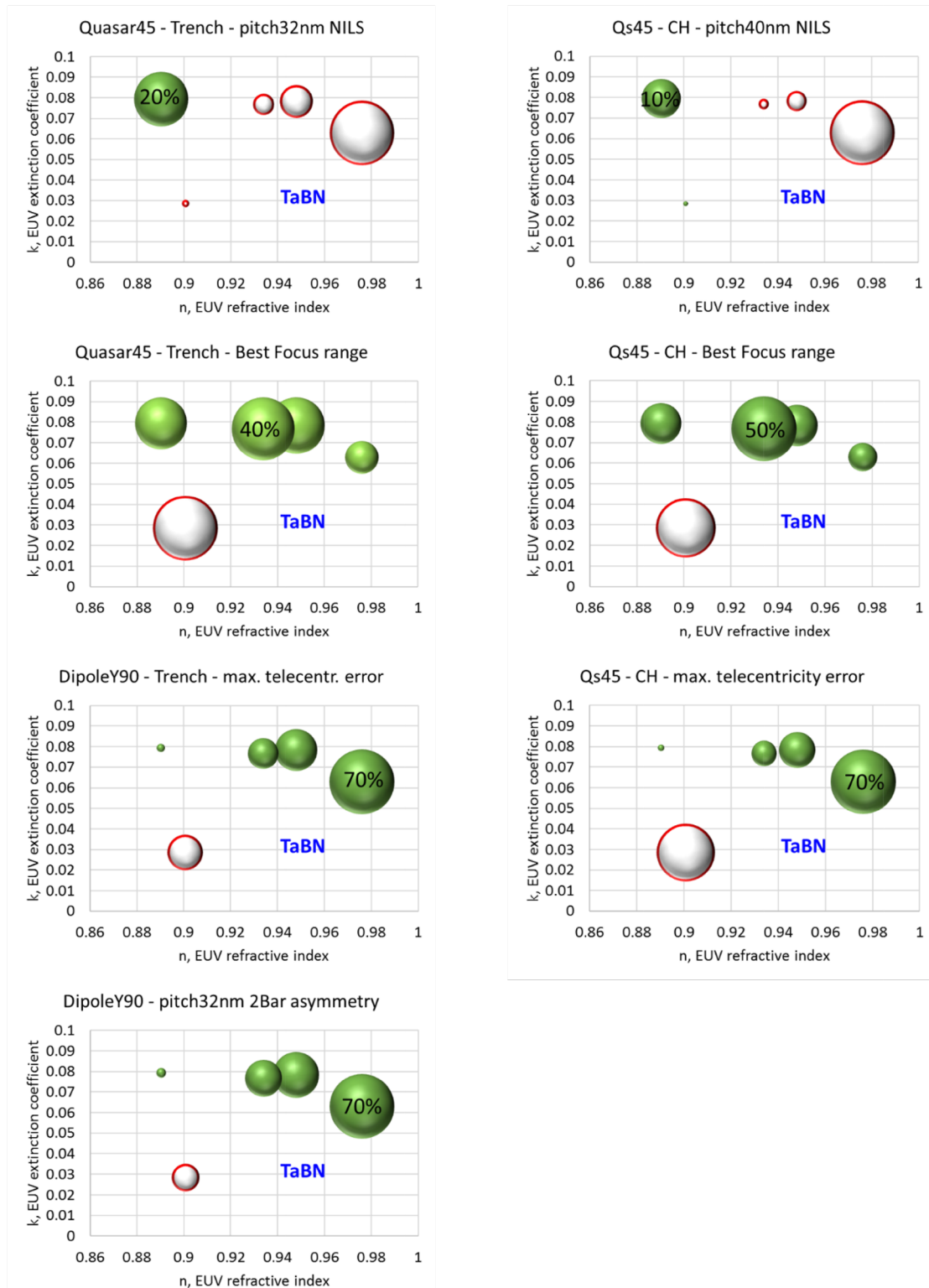


Figure 3. n&k plots with the change in the imaging metric of the 32nm n&k absorber mask compared to the reference 60nm TaBN absorber mask. The imaging metric, feature type and illumination at NA0.33 as indicated above the plots.

1. Introduction

Over the recent years the knowledge has grown and spread on mask-induced imaging effects, experimentally observed in EUV lithography for N5 dimensions and beyond, with the current EUV mask^[1-9]. More precisely,

the current Ta-based absorber is at its limit for imaging extendibility. Thinning down below 50nm Ta-based absorber thickness will reduce the amount of absorbed light, reduce the NILS and increase best focus variation through pitch^[10,11]. Although the current Ta-based mask has proven benefits from mask technology point, the imaging performance

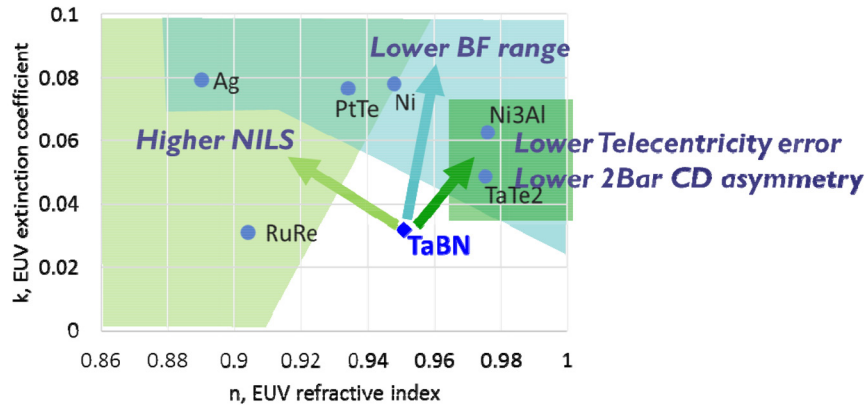


Figure 4. $n&k$ plot indicating three regions with expected improved imaging performance compared to the reference TaBN.

towards next technology nodes can benefit from mask optimization. The industry needs to reconsider the EUV mask concept as a mitigation of mask-induced imaging effects (by balancing the diffraction for all features and pitches simultaneously).

Different concepts have been introduced over the recent years, from tuning the Mo/Si multilayer mirror periodicity^[12], to changing the multilayer mirror materials to Ru/Si^[13] over embedding the absorber in the multilayer mirror^[3] and the etched multilayer mirror mask^[14,15]. Realizing these mask concepts in production worthy environment turned out difficult. Today, the focus of the industry is on novel mask absorber as the most realistic implementation towards future technology nodes. This became clear in the 2018 imec organized forums gathering the EUV blank supply chain to initiate a mask absorber change^[16]. In this paper we report the careful evaluation, combining rigorous imaging simulations and experimental absorber material tests, of potential novel EUV mask absorbers.

In Section 2 we motivate the choice of alternative mask absorbers based on their imaging performance compared to the reference Ta-based mask for typical N5 building blocks at NAO.33. Section 3 details out the mask absorber requirements and we demonstrate our experimental evaluation methodology flow using our engineered, novel EUV mask absorbers. Section 4 summarizes the main findings of this paper and points to a path forward in the absorber material down-selection.

2. Imaging Gain in EUV Mask Absorber Change

In this Section we highlight the benefit of a mask absorber change by projecting a wide range of imaging simulation results onto the material optical property space. In the first sub-Section we identify $n&k$ regions of interest for imaging improvement. In the second sub-Section we dive deeper into the simulation results of foundry N5 building blocks using different mask absorber $n&k$. This leads us to the identification of materials with predicted imaging gain.

2.1 Material space vs. reference TaBN mask absorber

When we look for alternative mask absorber materials from an imaging perspective, we represent the material options by their EUV optical properties, $n&k$, as these are – together with the absorber thickness – accountable for the imaging response in rigorous lithographic simulators. Figure 1 plots the EUV $n&k$ space with the measured values for TaBN^[17].

From TaBN as starting point we can improve to materials with higher EUV extinction coefficient, which allows for absorber thickness reduction and consequently smaller shadowing effect due to its increased EUV absorption. Tuning the refraction index of the material allows in one direction to reduce the phase deformation, when n is matched to vacuum, and in the other direction to increase the contrast, when enhancing the phase shift character of the material.

2.2 Imaging performance of $n&k$ regions vs. reference TaBN mask absorber

We studied the imaging performance of various mask absorbers in the three regions as indicated in Figure 1 for foundry equivalent N5 dedicated building blocks using the rigorous mask 3D simulator S-Litho EUV (Syn-

opsys)^[18] including our calibrated and experimentally validated mask Mo/Si multilayer mirror model presented in^[19]. Table 1 depicts the illumination settings at NAO.33 and selected feature types and ranges. The 60nm reference TaBN mask absorber is compared to candidate absorber materials at 32nm thickness with $n&k$ combinations as represented in Table 1 (c).

The imaging metrics sensitive to mask 3D effects evaluated through pitch for the three feature types and two illumination settings are the best focus variation, the NILS, the telecentricity error (i.e., pattern shift through focus), and the two bar CD asymmetry through focus. Figure 2 shows the imaging behavior using the 60nm TaBN reference mask. The range of best focus through pitch amounts to 40nm, while the maximum telecentricity error reaches – 13mrad and two bar CD asymmetry through focus at the smallest pitch of 32nm extends to 48mrad. Improving the imaging performance compared to the reference mask implies that the NILS needs to increase, while the best focus range, the maximum telecentricity error and two bar CD asymmetry through focus need to decrease. The relative change of each of these metrics by changing the absorber material with respect to the reference is plotted in $n&k$ plots in Figure 3. The size of the circle symbols is relative to the reference TaBN. The green color implies an improvement of the metric, while a red circle refers to a deterioration of the metric compared to the behavior of the reference mask. Also indicated in each plot is the value of the largest relative improvement. Combining all the imaging results allows us to identify regions in the $n&k$ space with expected imaging improvement compared to the reference mask. The resulting plot is shown in Figure 4. A region of increased NILS can be observed for materials with a refraction index n smaller than that of TaBN. This can be attributed to a destructive interference of (increasingly) phase shifted light in the absorber area and light in the vacuum region of the mask. Moreover, the decreased refraction index contrast between the absorber material and the vacuum caused a more pronounced waveguide effect for the light in the vacuum spaces and a further increase of the NILS. This finding supports earlier work^[2] where a co-optimization of mask material and illumination through multi objective optimization led to the improved imaging of an attenuated phase shifting mask compared to the reference Ta-based mask. A region of smaller best focus range through pitch can be found at higher extinction coefficient k than TaBN due to stronger EUV absorption over a smaller absorber thickness minimizing the phase impact. Lower telecentricity errors and two bar CD asymmetry through focus can be found at a refraction index n close to 1 due to the phase matching of the absorber material to the surrounding vacuum. The weight given to each optimization metric determines the optical $n&k$ region, although increasing the extinction coefficient k compared to TaBN points to a general improvement.

Within each of these regions we engineered and characterized potential EUV mask absorber materials. Ag, Ni and PtTe reside in the region of high extinction coefficient. Ni₃Al and TaTe₂ exhibit a refraction index approaching one while RuRe is positioned in the attenuated phase shifting region. Figure 4 illustrates the $n&k$ of the materials studied in more detail in the next Section.

3. EUV Mask Absorber Requirements

Mask absorber change is a complex and difficult process in the mask

Table 2. Investigated mask absorber requirements with their impact on EUV lithography and the testing methodology.

Characterization	Evaluation	impacts	Testing methodology
Film morphology	Crystallinity Surface composition Surface roughness	Line edge roughness Film stability Out-of-band	TEM, IS-XRD XPS, TEM AFM
Optical constants	EUV $n&k$	Imaging	EUV reflectometry
Mask durability	EUV & H* interaction Wet clean	Usage in scanner Mask cleaning	H* cleaner, high power EUV ICP-MS
Mask patterning	Traditional or disruptive	Pattern profile	RIE or ASD

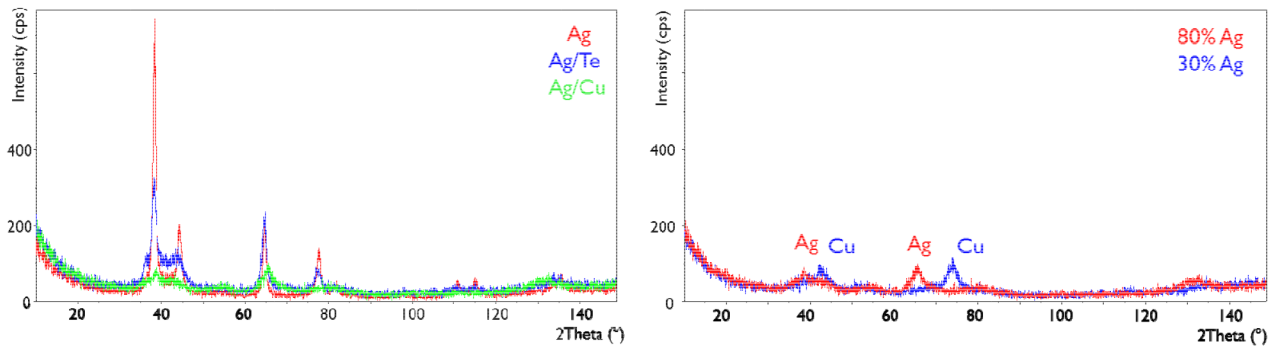


Figure 5. (a) XRD spectra for a single Ag layer, Ag/Te and Ag/Cu multilayers. Ag/Cu multilayers show very low crystallinity. (b) XRD comparison of crystallinity in low and high Cu containing Ag/Cu multilayers.

making technology, since the candidate material needs to fulfil a diversity of requirements to guarantee success throughout the complete EUV mask life cycle. Blank manufacturing, mask fabrication and qualification, and mask use pose various requirements on the candidate absorber. A more extensive list can be found in [20]. Table 2 lists the requirements we investigated in more details in the following sub-Sections.

3.1 Film morphology

Single metal films deposited by physical vapor deposition (PVD) suffer from full-layer crystallization and island-formation [1,20]. In the absorber films, studied in this work, the poly-crystallinity is reduced by multilayering or alloying.

Ag-based layered structures have been developed as potential alternative binary absorbers. Ag exhibits one of the largest extinction coefficients at 13.5nm, and as such is of interest for absorber development. The problem of full layer crystallization and islanding/roughness development during sputter deposited Ag growth was addressed by using an adhesive layer, such as Te or Cu. Further build-up of crystallinity and roughness is repressed by using a multi-layered structure, with individual layers of a few nanometers. Structural characterization of the Ag/Te system with X-ray diffractometry (XRD) (see Figure 5(a)) showed moderately reduced crystallite sizes, compared to pure Ag, coupled to a surface roughness with peak-to-valley in the few-nanometer range. To further reduce the film crystallinity and roughness, Ag/Cu multilayers have been developed that exhibit nanometer size crystals, have a rms roughness of 0.25nm, and are less prone to oxidation than Te. Changing the fraction of Ag and Cu only shifts the balance from nanometer size Ag to nanometer size Cu crystallites, both far away from full layer crystallization, as presented in Figure 5(b). The ratio of Ag to Cu can be further explored in order to tailor the optical response of the multilayer absorber, and as such Ag/Cu multilayers provide from this perspective a promising candidate for application as binary absorber material.

Alloying the suitable optical property of individual elements in a stable composition is another approach to reduce the film poly-crystallinity. In our earlier work we already reported on the development, characterization and imaging improvement of Ni-Al alloys [20]. To ensure high EUV absorption, we engineered metal telluride alloys with at least 50% Te due to its high EUV absorption. Our development includes Ru-alloys intended for

EUV attenuated phase shifting materials [2].

To increase the chemical stability of Te, we have investigated the noble metal telluride Pt-Te. The transmission electron microscope (TEM) image in Figure 6(a) shows the quasi-amorphous morphology of as-deposited PtTe, which is confirmed by in-situ XRD (IS-XRD) measurements, where it is shown that recrystallization occurs at 210°C.

We investigated the feasibility of Ta-telluride. The IS-XRD measurements and TEM image in Figure 6(b) prove the quasi-amorphous morphology of as-deposited Ta-Te alloy. A change in background intensity can be detected above 350°C, which will be discussed in more detail in Section 3.3.1. Furthermore, the presence of oxygen is detected through the full absorber layer.

As-deposited Ru-alloys exhibit a high degree of poly-crystallinity, visible in the TEM image of Ru3Re in Figure 6(c). This crystalline phase remains the same towards 500°C, based on the IS-XRD measurements. Doping with smaller elements, such as N and P, might reduce the crystallinity, by breaking the Ru crystal lattice through their size difference [21-22].

3.2 EUV optical properties

The EUV optical properties of the mask absorber material and its thickness determine its ability to mitigate mask induced imaging effects. EUV reflectance measurements through incidence angle and wavelength (from 10nm to 16nm) are conducted in the lubrication free EUV reflectometer of PTB at the soft X-ray radiometry beamline at the BESSY II storage ring [17]. The obtained surface plot, presented for three alloys in Figure 7, is fitted to provide the $n&k$ values in the wavelength range, as well as the thickness of the individual layers in the film. The accurate $n&k$ measurement of the engineered alloys is essential to correctly predict the imaging impact of these potential mask absorber films in rigorous lithographic simulation (cf. Section 2.2).

3.3 Absorber durability

During normal mask usage in the fab the mask absorber must stay unaltered in scanner conditions under normal operation. Additionally, the mask needs to withstand several wet conditions, when cleaned for removing surface particles. We have a testing methodology in place to assess the thermal stability as well as the stability under hydrogen environment and in solutions of the candidate material films. This stabil-

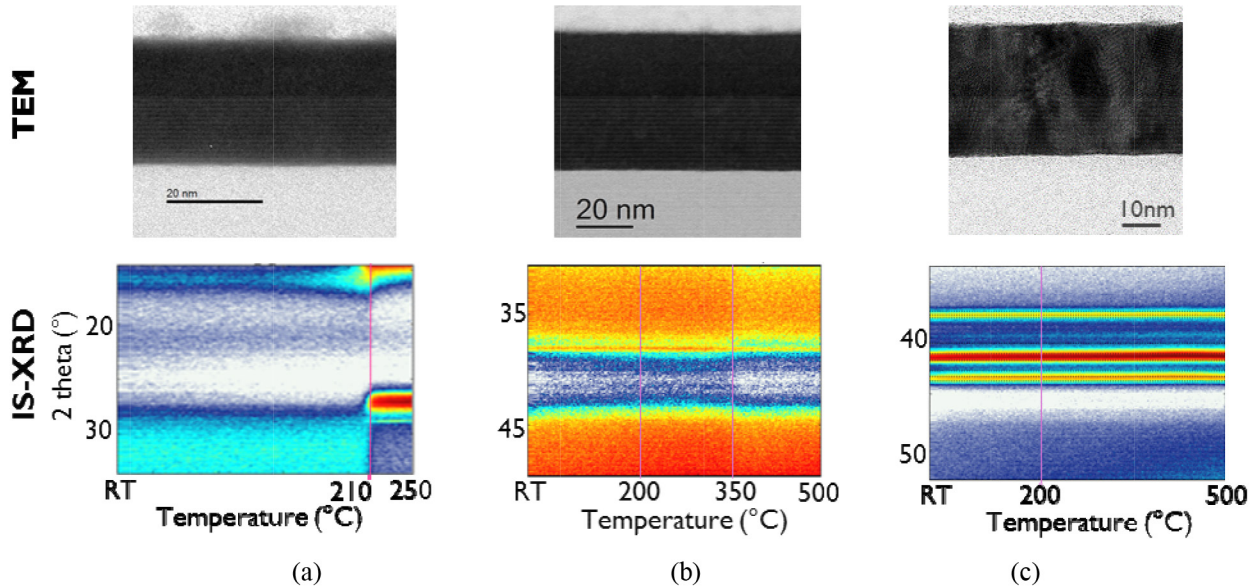


Figure 6. Top: TEM image of as-deposited (a) PtTe, (b) TaTe₂ and (c) Ru₃Re film. Bottom: corresponding IS-XRD plot.

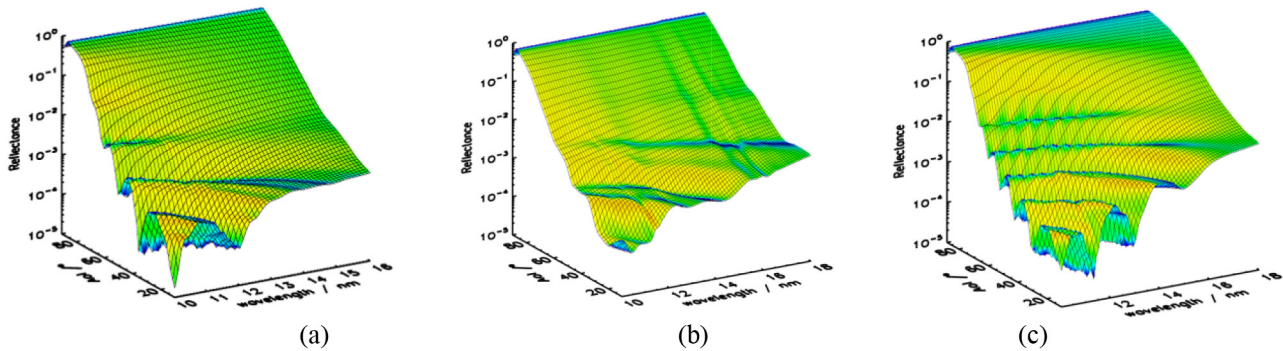


Figure 7. EUV reflectance (in log10 scale) as function of wavelength and incidence angle of a ~30nm thick (a) PtTe film, (b) Ni₃Al film and (c) Ru₃Re film on Si substrate.

ity quantification of the engineered absorber films allows us to make a proper absorber selection.

3.3.1 Thermal stability

The temperature of the mask in the scanner or in storage will vary, but it may not impact the material morphology.

We quantified the lifetime of the candidate absorber films by calculating the recrystallization activation energy through a Kissinger analysis^[23]. For the PtTe film we extrapolated the lifetime until recrystallization at different temperatures as presented in Figure 8(a). At a constant temperature of 80°C the PtTe film will recrystallize after 450 years, which is beyond the typical mask lifetime in a fab.

Under thermal loading the alloy might suffer from material loss. For the TaTe₂ and PtTe films the Te loss was measured at different temperatures, as shown in Figure 8 (b-c). Te becomes volatile just above 400°C and therefore in both tellurides a significant Te loss is detected at 500°C. At a temperature of 250°C the noble metal telluride PtTe seems to stabilize the Te better than the Ta-telluride, where some Te loss is noticed.

From the thermal stability tests on the engineered absorber films we learned that PtTe and TaTe₂ will stay unaltered during typical mask thermal conditions (i.e., below 150°C).

3.3.2 Wet conditions

The candidate mask absorber must stay stable during typical mask cleaning. We selected two solutions currently used in mask cleaning: (1) de-ionized water (DIW) at pH 5.7, (2) 1% NH₄OH at pH 11.4. First the

samples are fully submersed in a beaker with the solutions for ~24 hours. After visual inspection the films are measured by XRR to determine film thickness loss, roughening of density change. The stability of the alloys in wet conditions depends on the reactivity of each surface element. Aluminum metal reacts under both acidic and alkalic conditions, which impacts the reaction of Ni₃Al in both tested aqueous solutions. TaTe₂ and PtTe are more stable in the solutions due to the stability of Ta and Pt respectively.

Next, the PtTe film is characterized by the highly sensitive technique of inductively coupled plasma mass spectrometry (ICP-MS) to determine the rate of material loss in each aqueous solution. Figure 9(a) shows the measured Te dissolved in both aqueous solutions after three different time durations.

From the slope through time we calculate the Te dissolution after a full day of submersion to be less than 0.4nm in DIW and less than 1nm in 1% NH₄OH. The quantification of absorber material loss after multiple mask cleans allows us to perform an imaging sensitivity study in a next stage.

3.3.3 Scanner conditions

In the scanner the mask, and thus the mask absorber material, is exposed to high EUV power in H₂ environment. Under these operation conditions hydrogen radicals (H*) can be formed and these H* can react with the absorber material. However, the absorber material is not allowed to form volatile contaminants or to change its film morphology. Therefore, we conducted a first assessment by exposing our alloy films to a strong H* environment in imec's EUV tech hydrogen cleaner^[20]. The elemental

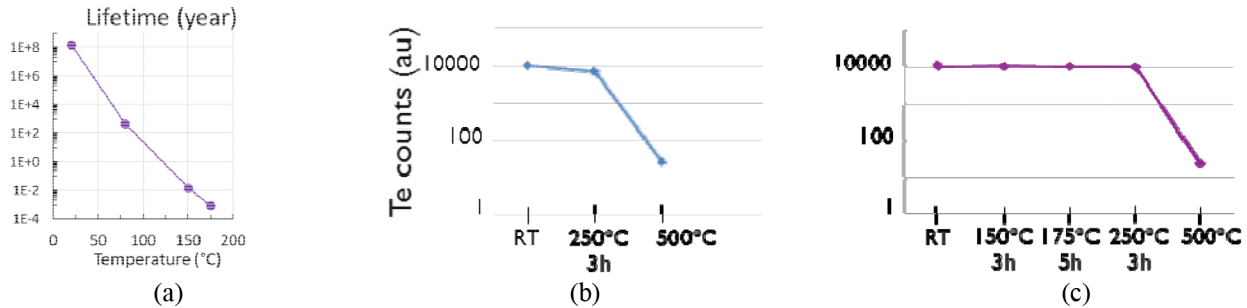


Figure 8. (a) Lifetime until recrystallization of PtTe film vs. temperature. Te loss measured at different thermal loadings for (b) TaTe₂ and (c) PtTe film.

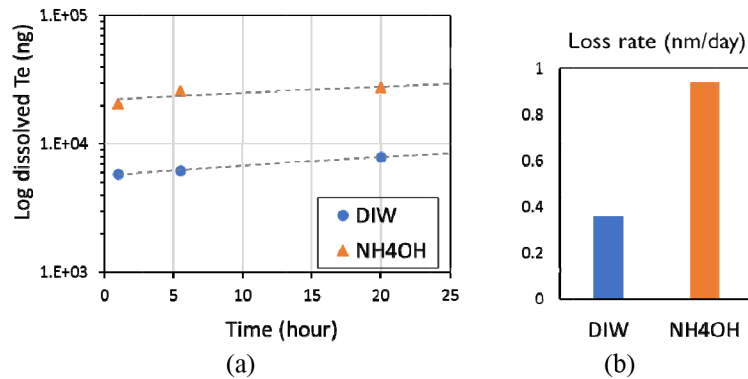


Figure 9. (a) Dissolved Te measured by ICP-MS in two different aqueous solutions after three different time durations. (b) Calculated Te loss rate per full day in two different aqueous solutions.

content of the films before and after exposure was determined by Rutherford backscattering spectrometry (RBS) and the relative changes are plotted in Figure 10. The Ni₃Al and Ru₃Re films remain intact after ~24 hours H⁺ exposure, while the tellurides lose Te content due to reaction to H⁺. Again, the noble metal telluride PtTe seems to bind the Te stronger in the bulk than the Ta-telluride.

The individual H⁺ test with the novel absorber films provides a rough estimate of possible interactions. However, the test conditions to pure H⁺ environment are too aggressive compared to the actual scanner conditions. A dedicated test setup is required to effectively assess the impact of combined high EUV power and H₂ atmosphere. We performed a feasibility test in an irradiation chamber at the beamline of PTB where the PtTe films were exposed to variable conditions of combined EUV power density and H₂ gas pressure^[24]. Our initial results in Figure 11 are obtained by characterizing the films after EUV+H₂ exposure by X-ray photoelectron spectroscopy (XPS).

Two groups of results can be observed: the reference PtTe film and the PtTe films only exposed to EUV show strong oxidized Te peaks at the PtTe surface, while a combination of EUV and H₂ gas seems to reduce the te-oxide on the PtTe surface to metallic Te, resulting in equal Te-oxide and metallic Te peaks.

These initial results indicate the sensitivity of the materials to the combined exposure of EUV power and H₂ environment, but the samples need additional characterization to quantify its impact. Further optimization of the test conditions and matching to actual scanner conditions is ongoing.

3.4 Absorber patterning

To emphasize the importance of absorber patterning we assessed experimentally the feasibility of two patterning approaches. Subtractive patterning is the traditional way of patterning the mask absorber, while additive patterning can be considered a disruptive manner for the mask making technology.

3.4.1 Subtractive patterning

Mask absorber patterning is typically achieved with reactive ion etching (RIE), which is a chemical assisted physical etching process. By form-

ing volatile compounds, the etching is material selective. The physical etching by ion beam sputtering can pattern through thin metal layers anisotropically, but it is not extremely material selective. From earlier work^[1] we experienced the challenges of metal patterning through RIE etching. Now we focus on the chemical component of this etch technique of using halogen-based plasma only, without a carrier gas to reduce the physical etch, and to demonstrate volatile formation with the elemental components of the absorber alloy.

In a first etching test a 1μm thick patterned resist film is used as mask for the chemical etching of the ~30nm thick material film in an RIE tool. The cross-section SEM images in Figure 12 illustrate the outcome for the pure Te, the PtTe and the TaTe₂ film.

The pure Te film is isotropically etched in the plasma and is therefore likely etchable by RIE. The Ta-telluride is also removed in the chemical etch process. This is a strong indication that alloys with Te and other chemically etchable materials are patternable in a subtractive way. However, the Pt-telluride sample suffers from fences on the resist sidewalls and from residues in the removed trenches, which is typical for metal etching with limited volatile by products, as we also observed for Ni etch^[1].

High-*k* metals, like Ni and Pt, are difficult to etch selectively with known chemistries. Direct metal etch process development is needed to solve these patterning challenges.

3.4.2 Additive patterning

Because subtractive metal patterning poses challenges on selectivity and chemical volatility of metal elements, we took a disruptive approach of metal-on-metal area selective deposition (ASD) for mask patterning purpose^[25].

Since our simulation study converges to high-*k* metals as candidate mask absorber materials, and the multilayer mirror of an EUV mask is capped with Ru, which is a metal, we can grow in a controllable way metal selectively on metal by using a dielectric template through metal electroless deposition^[26]. The flow is exemplified in Figure 13.

We start with sacrificial patterning in a dielectric layer (e.g., SiO₂). The quality of template pattern will determine the sidewall and edge of the

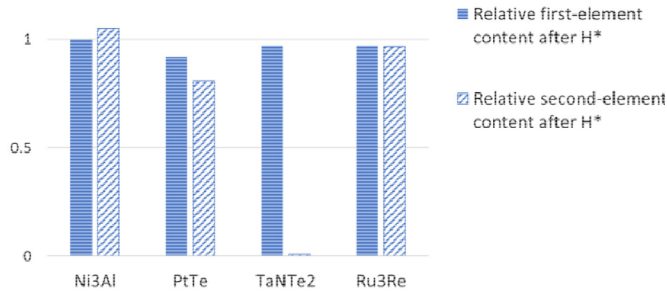


Figure 10. Relative elemental content after more than 24 hours H^+ exposure as determined by RBS.

final metal features. The dielectric patterning enables further CD/pitch scaling with good sidewall definition. In a next step the ASD is performed where the metal (i.e., the mask absorber material) grows in a controllable way on the open Ru metal areas in between the template. In our example cobalt ASD is performed and in Figure 13 (b) the trenches in the template are filled with cobalt without Co deposition on top of the template. Furthermore, the Ru capping layer is not damaged by additive patterning, contrary to etching. In the last step the template is removed to result in the final Co metal absorber pattern. Characterization of the Co ASD in this example shows a fully amorphous cobalt pattern, as crystallization is confined by the dielectric template. This is an additional advantage of the additive patterning technique, while Co PVD is polycrystalline. Additive patterning has potential to create amorphous metal patterns in the Ru cap of the multilayer mirror. Further optimization is required to demonstrate the compatibility with mask size, e.g., in terms of thickness uniformity and defectivity.

4. Conclusion

In this paper we proposed novel EUV mask absorbers based on a combined assessment of their imaging performance and experimental behavior in EUV lithography conditions. We identified EUV $n\&k$ regions for mask absorbers around 32nm thickness with improved mask induced imaging effects compared to the current Ta-based mask absorber. For each of these improvement regions, we engineered novel mask absorbers. The PtTe alloy and Ag-based multilayer have a high EUV absorption and therefore will suffer less from best focus variations. NiAl and TaTe alloys match at EUV wavelength their phase close to vacuum resulting in reduced telecentricity errors and two bar CD asymmetry through focus. The RuRe alloy is put forward as potential attenuated phase shifting material for EUV masks expected to increase NILS at reduced dose-to-size with the proper mask-illumination optimization.

We presented the experimental testing methodology geared to address several essential mask absorber requirements to allow for a material down-selection. Our testing flow assesses the durability of the film morphology under different thermal, EUV, hydrogen, and cleaning conditions, typical for mask environment. We found that the most interesting materials from lithographic perspective pose challenges on traditional mask etching technology. Adapting towards etchable materials creates a trade-off in material durability and subtractive patterning. Therefore, we introduced the concept of additive patterning for mask absorbers.

Our next goal is to find solutions – through collaborative efforts – for the absorber patterning challenges of the proposed novel mask absorber materials.

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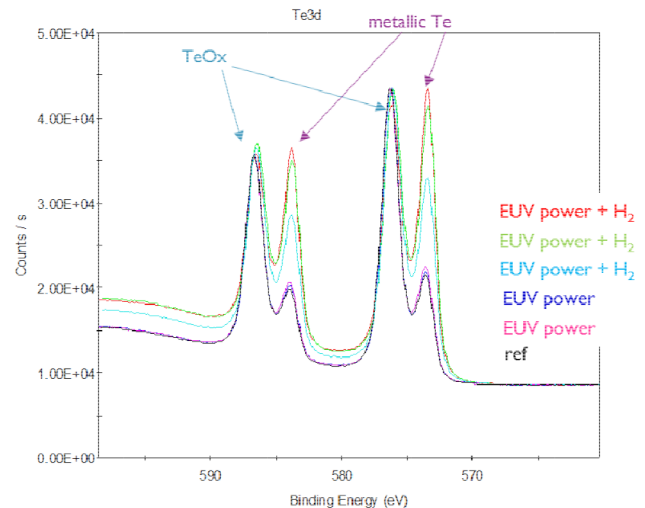


Figure 11. Normalized XPS spectra of Te3d peaks from the reference and five PtTe films exposed to different EUV+ H_2 conditions.

and Dr. BT Chan (imec). We appreciate the support of Dr. G. McIntyre, Dr. K. Ronse, Dr. S. Van Elshocht (imec), and Prof. Dr. M. Heyns (KU Leuven).

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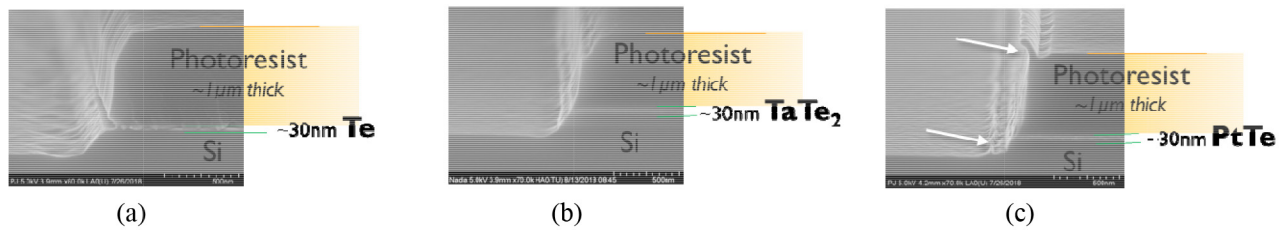


Figure 12. Cross-section SEM images after chemical etch in halogen-based plasma of ~30nm thick (a) Te, (b) TaTe₂, and (c) PtTe film on Si substrate and with a 1 μm thick resist pattern on top.

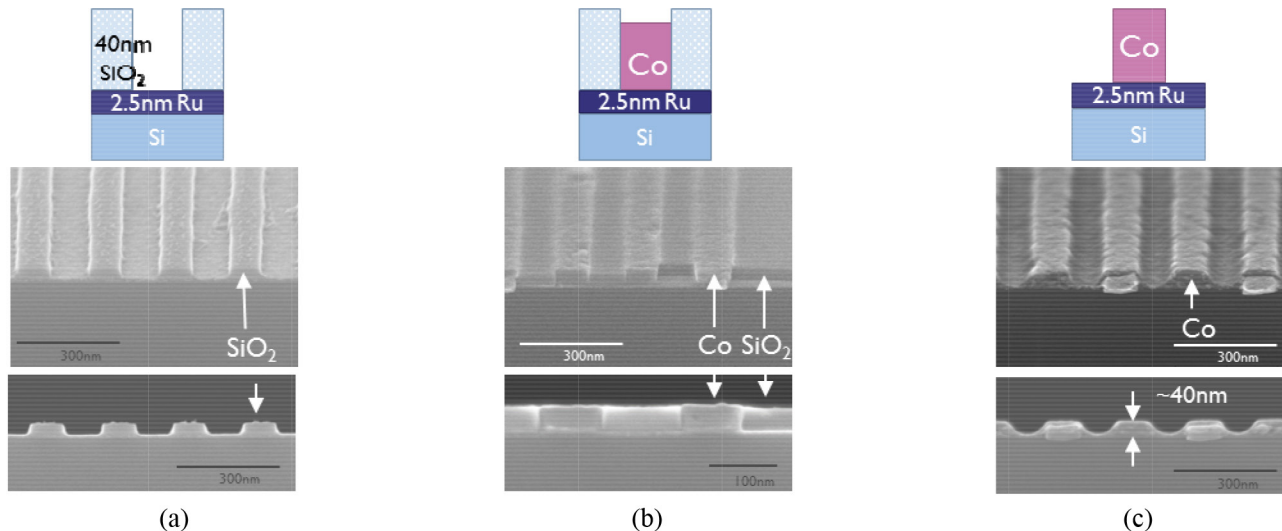


Figure 13. Simplified flow for metal-on-metal ASD on 200nm pitch equal lines/spaces. (a) Template patterning; (b) cobalt ASD; (c) Co pattern after template removal.

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Industry Briefs

■ Plessey Announces 2.5micron Pitch MicroLED Display

September 2019

Plessey, a UK-based microLED developer, has produced a record-breaking 2.5-micron pixel pitch, ultra-high resolution microLED display. The 2000x2000 pixel display is the latest example of the company's monolithic GaN-on-Silicon technology,

Clive Beech, senior director, business development, at Plessey, said: "Pixel pitch is key to the physical size of large field displays and to the resolution of the viewed image. These are key attributes in AR systems. The 2k2k display scale can be realised in a compact physical form factor and the 2.5micron pixel pitch achieves image features with smooth borders and fine detail."

Plessey's GaN-on-Si technology brings many benefits. The low thermal resistance of the silicon substrates allows highly efficient heat extraction resulting in lower junction temperatures with high reliability. The technology also allows impressive energy efficiency, high resolution and unsurpassed contrast. With its similarity to large scale Silicon IC processing the technology can be scaled to progressively larger wafers, improving cost, uniformity and yield and taking advantage of the latest advances in Silicon wafer processing tools of the volume IC industry.

Plessey's roadmap includes the production of a full RGB display all on one wafer by 2020's Consumer Electronics Show.

<https://compoundsemiconductor.net/article/108723>

■ Highest Efficiency For Flexible CZTSSe Solar Cell

September 2019

Scientists at DGIST (Daegu Geongbuk Institute of Science and Technology, Korea) have achieved 11.4 percent photoelectric conversion efficiency for a flexible CZTSSe thin-film solar cell. This is believed to be the highest efficiency for this material. CZTSSe, a chalcogenide, it is of interest for next generation solar power technology. However, photoelectric conversion efficiency for flexible CZTSSe's had not exceeded 10 percent due to the spread of impurities inside the flexible substrate and de-lamination.

One of the biggest achievements of this research, according to the scientists, is increased efficiency by changing the existing three-stack structure of CZTSSe thin-film solar cell precursor into a multi-layered structure and by improving the voltage characteristics and uniformity. Moreover, it has the problem with uniformity degradation when a large-area process is applied to thin-film solar cell, but technology in this research improved not only efficiency but also uniformity.

Jin-Kyu Kang, the research project manager, said: "As there is growing interest towards environment and resources are used unlimitedly these days, it is a very meaningful achievement that we have developed a thin-film solar cell using widely available, eco-friendly materials. DGIST plans to open the Research Centre for Thin Film Solar Cell based on this achievement."

<https://compoundsemiconductor.net/article/108720>

■ Design: DRAM, NAND and Emerging Memory Technology Trends in 2019

Jeongdong Choe, PhD., TechInsights, Ottawa, Canada

DRAM cell scaling down to 17nm has already been productized by major DRAM players including Samsung, Micron and SK Hynix. Currently, they are developing n+1 (16nm) and n+2 (15nm or beyond) called 1z, 1a and 1b generation devices, which means the DRAM cell design rule might be able to further scale down to sub-15 nm without adopting Extreme Ultraviolet (EUV) lithography for DRAM cell patterning.

The cell design scaling down process is slowing due to many scaling issues including patterning, leakage and sensing margin. And so far, EUV adoption on DRAM process is not cost effective. It appears that the 14nm DRAM cell design rule would be the last node if DRAM cell architecture keeps the current 1T1C with B-RCAT and cylindrical capacitor.

Bit density on DRAM die reached 0.237 Gb/mm² on Samsung's 1y nm LPDDR4X 8 Gb die, which is a 25.4% increase from the 1x LPDDR4X die. Micron recently introduced its 1y nm 8 Gb DDR4 DRAM die with 0.205 Gb/mm², a 22.7% increase from its 1x DDR4 die.

As demand for embedded memories and MCU applications for AI and IoT increases, emerging memory technologies are also on the rise. In addition, Intel has been applying their XPoint memory devices into NAND and storage products such as Optane SSD, Optane DC SSD, DC PM and even NVDIMM application. Emerging memories consist of MRAM, including STT-MRAM and SOT-MRAM, PCRAM including XPoint, ReRAM including CBRAM, OxRAM and Memristor, FeRAM and others including NRAM.

<https://www.semiconductor-digest.com/2019/09/13/dram-nand-and-emerging-memory-technology-trends-and-developments-in-2019/>

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