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Actinic patterned mask defect inspection for EUV lithography

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ABSTRACT

As extreme ultraviolet (EUV) lithography enters high volume manufacturing, the semiconductor industry has considered a lithography-wavelength-matched actinic patterned mask inspection (APMI) tool to be a major remaining EUV mask infrastructure gap. Now, an actinic patterned mask inspection system has been developed to fill this gap. Combining experience gained from developing and commercializing the 13.5nm wavelength actinic blank inspection (ABI) system with decades of deep ultraviolet (DUV) patterned mask defect inspection system manufacturing, we have introduced the world's first high-sensitivity actinic patterned mask inspection and review system, the ACTIS A150 (ACTinic Inspection System). Producing this APMI system required developing and implementing new technologies including a high-intensity EUV source and high-numerical aperture EUV optics. The APMI system achieves extremely high sensitivity to defects because of its high-resolution, low noise imaging. It has demonstrated a capability to detect mask defects having an estimated lithographic impact of 10% CD deviation on the printed wafer.

1. Introduction

Actinic patterned mask inspection (APMI) has been discussed for more than 20 years in research on mask defect characteristics, mask defect management and mask qualification control.¹ The IC industry believes that final inspections and qualifications of EUV masks requires inspection at the 13.5nm wavelength, the same wavelength used for EUV lithography. Until now, the IC industry has been bridging the gap by stretching the application of existing mask inspection tools and wafer inspection processes. Using reflected light, EUV masks can be inspected with modified DUV mask defect inspection tools originally designed to inspect 193nm immersion masks. However, because of the large difference in wavelength between EUV and DUV, defect management relying on DUV tools is quite difficult since they cannot detect all defects that print in the wafer lithography process. It is challenging to conduct mask defect analysis based on wafer printing tests. High-sensitivity wafer inspection is extremely challenging because it is difficult to find and classify 10% delta-CD defects.² Inspecting a 13.5nm EUV mask with 193nm DUV is analogous to inspecting a 193nm mask with a mid-infrared wavelength of 2760nm. As such, DUV patterned mask defect inspection systems do not claim to detect defects with printing impact such as phase defects in the EUV multilayer.

One of the merits of using actinic inspection is its high image resolution. Because of the much shorter wavelength, actinic inspection provides high resolution and high contrast images. Many studies on actinic defect inspection show promising results due to high imaging performance in both brightfield and darkfield



Figure 1. History of actinic blank and patterned mask defect tools leading to the APMI system.

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EDITORIAL

BACUS Entertainment

Peter Buck, Mentor Graphics Corp.

2019 Photomask Technology & EUV Lithography conference attendees observed what many considered a major change to the banquet this year – the traditional BACUS entertainment was dropped. The combined BACUS and EUVL steering committees decided to try something different this year. The conference has evolved – international in attendance, expanded in scope with the co-location of the EUV Lithography conference, and with the influx of many attendees without the historical reference of BACUS. The context of the traditional entertainment – skits and song that roast individuals and organizations – is beloved by many but at the same time not understood or appreciated by others. The BACUS & EUVL steering committee recognizes this dichotomy and tries to accommodate both perspectives. This provides the opportunity to diversify – to try different things, to experiment, to take chances. Holding the 2019 banquet at the Monterey Bay Aquarium was such an experiment, with the opportunity to hold conversations in an inspiring atmosphere with nature providing the entertainment. We will continue to evolve as a conference and as a technical community, and we will continue to hone the conference experience to provide meaningful opportunities for all to engage professionally and to have a pleasant venue to meet socially.



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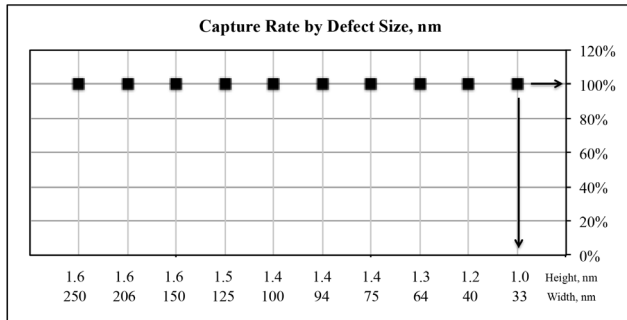


Figure 2. Inspection capability for phase defects. Defects are pit type and widths represent full width half maximum.

observations.^{3,4} Actinic inspection has also been used to examine the relationship between EUV mask defect detection signal strength and the defect's wafer printing impact.⁵ In this study, the printability of blank defects on an EUV mask is estimated by means of actinic imaging. It shows that there is a significant relationship between the signal intensity of EUV defects and the CD deviation on wafers, and the results correspond well with simulations. Actinic patterned mask defect inspection is the only method capable of detecting EUV patterned mask multilayer phase defects, which DUV patterned mask defect inspection tools cannot do.⁶ APMI is expected to provide the ultimate capability for EUV patterned mask inspection by detecting all types of printable defects. The capabilities of the other patterned mask inspection tools, DUV and e-beam inspection systems, have been evaluated.⁷ According to these evaluations, DUV inspection systems do not provide adequate image contrast for the small pattern pitch of the N5 node with 80nm half-pitch at mask scale. In addition, these evaluations conclude that single-beam e-beam inspection systems would require excessive inspection times to detect the required defect sizes.

EUV masks are inspected at various steps during the mask making process at mask shops and EUV mask quality control at wafer fabs. APMI is applicable to all of them. The first mask inspection is necessary after mask writing to detect hard defects having wafer printing impact. Since APMI has a capability to detect such defects, it is the preferred inspection method. APMI is also applicable to final qualification at mask shops, incoming inspection at wafer fabs and inspection conducted for mask quality control after exposure. The need for APMI is more evident when an EUV pellicle is used, since APMI is necessary for through-pellicle patterned mask inspection to detect particles added during pellicle mount and to monitor the contamination of the mask under the pellicle. Extremely thin (less than 50nm) polycrystalline silicon membranes are currently used as an EUV pellicle.⁸ These extremely thin silicon membranes do not transmit DUV light, eliminating the possibility of through-pellicle inspection with DUV mask defect inspection tools. However, the silicon membranes are adequately transparent to both 13.5nm EUV and visible light. However, the 380 to 740nm wavelength of visible light is too large to provide good image resolution. The sensitivity of visible light inspection is estimated to be insufficient to detect all of the defects that can print and kill chips. Whether there is a pellicle or not, APMI inspection is the best method to inspect EUV masks because of its ability to detect all types of EUV mask defects with high sensitivity.

2. Tool Configuration

2.1 Development history

The ACTIS A150 is the world's first actinic EUV patterned mask inspection system, but it draws on expertise accumulated over the years. This high-sensitivity and high-speed actinic patterned mask inspection system is a culmination of experience in the development of 13.5nm EUV actinic blank inspection (ABI) system and widely adopted DUV patterned mask defect inspection systems as shown in Fig. 1. The actinic inspection technologies used in the ACTIS A150 originate with the proof of concept of the ABI system that started in 2001 with MIRAI-ASET.⁹ Actinic inspection must be conducted in a vacuum since EUV light gets absorbed in the atmosphere. EUV optics must achieve an extremely low degree of surface error to meet the stringent imaging performance requirements. These challenges were overcome in the process of fabricating two prototypes and a high-volume

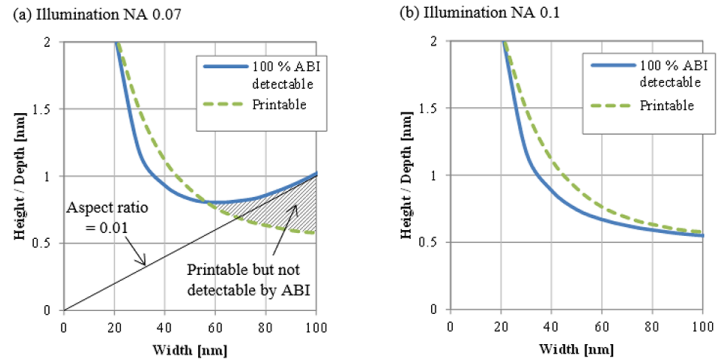


Figure 3. Detection capability for illumination NA of 0.07 and 0.1. Printable lines represent the limitation of printable defect sizes in 64nm line and space on masks by simulation.

manufacturing tool of the ABI system.^{10,11} Patterned mask defect inspection technologies have been developed for DUV masks. The MATRICES X800 series is a patterned mask inspection tool commonly used in wafer fabs to inspect DUV masks. The latest model MATRICES X8ULTRA has the capability to inspect EUV masks by introducing linear polarization and a smaller pixel size. It has the capability to detect 40nm absorber defects in N7 EUV masks.¹² In developing the ACTIS 150, we have drawn from both our DUV and EUV mask defect inspection technologies and experiences.

2.2 Actinic blank inspection technologies

The base technologies used for APMI originate from the development of actinic blank inspection tools. After 18 years of technology development, ABI tools are now used for EUV mask blank qualification by all EUV blank suppliers. For inspecting EUV mask blanks, the ABI tool has three characteristics. The first characteristic is phase defect detection capability. EUV mask blanks use Mo/Si multilayer stacks as their EUV reflective layer. There is a type of a defect called a phase defect that frequently occurs during the manufacturing of the multilayer stack. If undetected, these phase defects can print and kill all of the chips printed with that part of the mask. These phase defects can only be correctly detected by using the same 13.5nm actinic EUV wavelength as the lithography tool. Inspection tools that use other wavelengths do not have the capability to detect all of the killer phase defects. One reason for the sensitivity limitations of non-actinic tools is the rapid attenuation of non-actinic light in the Mo/Si multilayers. The second ABI characteristic is reasonable throughput under the limited power of the EUV light source. We developed a high efficiency illumination system and used dark field detection for high inspection throughput using limited EUV power. The ABI tool achieves defect inspection with a throughput of 45 minutes per blank. The third ABI characteristic is defect review capabilities. We developed a high magnification defect review mode to monitor defect images using a 10nm pixel size. In addition to dark field review, we installed a bright field review function. These review functions with actinic light are useful for defect analysis. For example, defect images with dark field high magnification are utilized for accurate defect location measurements for defect mitigation. Defect mitigation techniques are used to cover the defects under the absorber layer during mask pattern writing. This technique requires high-resolution imaging to determine the exact defect location on the blank. Another example is defect visualization by actinic light. High magnification actinic review images provide a multitude of useful information such as defect shape and size. All of these exceptional ABI characteristics contributed to the development of the APMI system.

2.3 ABI capabilities and application to APMI

Fig. 2 shows the ABI phase defect inspection sensitivity in 2013. The ABI high volume manufacturing (HVM) prototype tool had a capability to detect programmed phase defects with a 1.0nm height and 33nm width (full width half maximum).¹³ This programmed phase defect sensitivity result is adequate for multilayer mask blank inspection. However, additional study results have shown that ABI detection capabilities are highly dependent on illumination conditions.¹⁴ Defect inspection capabilities with different illumination angles are shown in Fig. 3.¹⁵ Low aspect defects are less detectable than high aspect defects with an illumination numerical aperture (NA) of 0.07 as shown in Fig. 3(a). Low aspect phase defects have lower angles of scattered light. The reason for the low detection sensitivity on low aspect ratio phase defects is that inadequate scattered

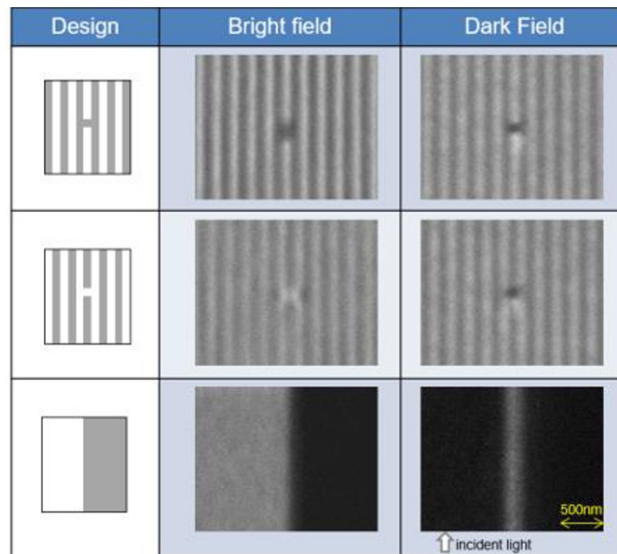


Figure 4. Pattern defect review with bright field and dark field. Upper pictures are an absorber protrusion edge defect, middle pictures are an absorber intrusion defect and the lower pictures are an absorber pattern edge.

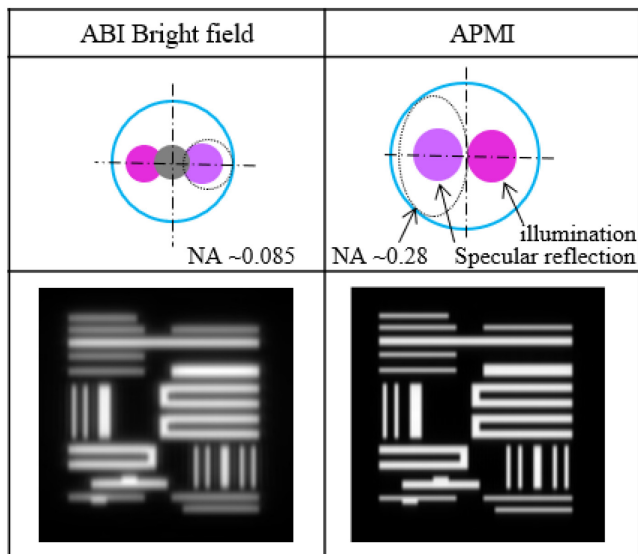


Figure 5. Imaging comparison between ABI BF and APMI. Line width: from 40nm to 80nm on mask.

light reaches the detector with the 0.07 NA illumination. As shown in Fig. 3(b), with an illumination NA of 0.1, adequate scattered light reaches the detector and the system will detect all of the printable defects that print with a 10% CD deviation on the wafer. This result verified that the ABI tool could be improved to detect low aspect phase defect by enlarging the illumination NA to 0.1.

The ABI HVM prototype tool is not only useful for blank inspection. Actinic review capability is another application for EUV mask development. For further actinic observation capabilities, high magnification review optics were installed. Two additional mirrors and control functions were installed in the imaging optics. One switching mirror was installed in the illumination optics for bright field defect review. The ABI tool supplies both low magnification (26x) and high magnification (1200x) imaging with both dark field and bright field imaging. Fig. 4 shows both dark field and bright field review images for 100nm half-pitch line-and-space patterns.¹⁶ It is difficult to identify the defect type (intrusion or protrusion) using dark field optics because dark field optics collect signals only from

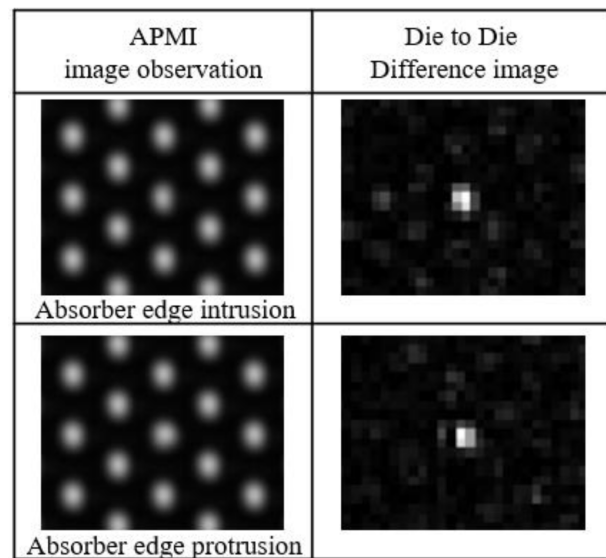
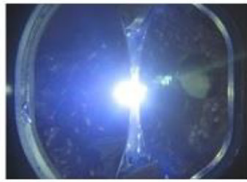


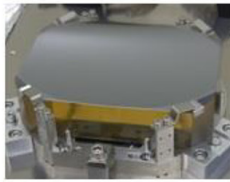
Figure 6. APMI defect observation capability in 80nm contact hole. Upper: 17nm absorber edge protrusion defect at center right. Lower: 24nm absorber edge intrusion defect at center right.

the pattern edges. On the other hand, intrusion and protrusion defects in the mask pattern can be classified by using high magnification bright field review because it provides reflectivity information. Therefore, high magnification bright field review provides useful information for lithographic impact prediction.

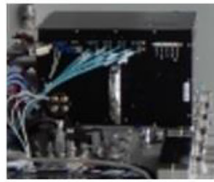
The ABI tool was used for initial studies of patterned mask defect inspection. Bright field observations were used for an optics requirement study. In this study, pattern visibility with the 0.085 NA ABI optics did not have enough resolution for patterned mask defect inspection. Therefore, we used simulation to estimate the optical requirements for APMI. Fig. 5 shows the results of simulations with diffraction effects but without taking optics fabrication errors and alignment errors into consideration. The linewidths varied from 40nm to 80nm. We concluded that even a theoretically perfect 0.085 NA lens would not have enough resolution for APMI defect review. Simulations showed that a larger imaging NA of 0.28 would produce EUV mask pattern images with significantly higher contrast. Therefore, we concluded that this higher NA would be effective.



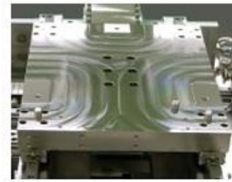
EUV source



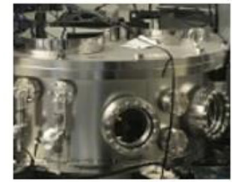
EUV illumination



EUV Detector



Stage control system



Mask handling system

Figure 7. Components of APMI tool.

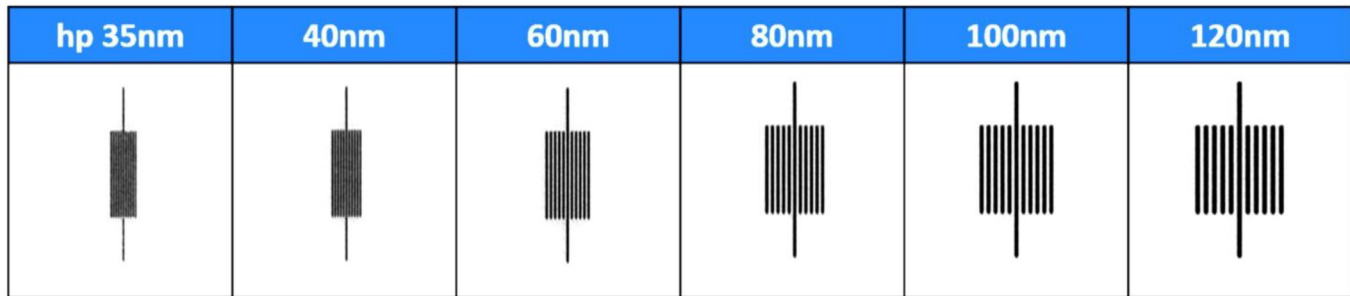


Figure 8. Images of line-and-space patterns.

tive in achieving the higher resolution required for EUV patterned mask defect inspection.¹⁵

We conducted other simulations taking fabrication errors and noise effects into account. 80nm mask contact hole patterns were used in these simulations. 17nm absorber edge protrusion defects and 24nm absorber intrusion defects were located respectively at the center right of these contacts as shown in Fig. 6. Die to die comparison images have enough signal to noise ratio for detecting these defects.¹⁷ Calculations of defect printability showed that both the 17nm absorber edge protrusion defect and the 24nm absorber edge intrusion defect produced a 10% CD deviation on the wafer. Therefore, we predicted that the APMI system would have the capability to detect mask defects that cause a 10% wafer CD deviation.

2.4 Tool configuration

Fig. 7 shows some of the major components of the ACTIS A150 system. These components and key technologies associated with them had to be developed for stable inspection performance. Unlike wafer exposure tools, actinic inspection requires an EUV light source that provides a small beam spot and high-brightness illumination. This is because EUV light needs to illuminate a small area on the mask to enable defect review with high-magnification imaging optics. For ACTIS, a tin-based EUV light source has been chosen because of its high conversion efficiency to generate high-brightness EUV light.

The development of illumination and imaging optics for actinic inspection was one of the most challenging tasks. New reflective EUV optics consisting of Mo/Si multilayer-coated mirrors and grazing-incidence mirrors were designed, developed and fabricated. To attain good imaging performance, the EUV mirrors must be fabricated with extremely small degrees of surface figure error and surface roughness. As explained in the previous section, the imaging optics needed a higher NA.

Additionally, the cleanliness of the mask-handling system must be high to meet the requirement of usage in a vacuum. Particle characteristics in a vacuum are different from those in the atmosphere. In the atmosphere, particles can be reduced by a combination of particle filters and airflow control. However, in a vacuum, where a very small amount of gas molecules exists, particles have long mean free-paths. We have constructed a system that keeps particle generation to a minimum and developed pumping and venting procedures to limit particle transports.

The EUV detectors are designed to operate at high speed and have a high level of sensitivity at 13.5nm wavelength. They are vacuum compatible. The stage control system is also vacuum-compatible and achieves the level of location accuracy that meets the requirements of imaging performance.

3. Inspection Performance

3.1 Image resolution of A150 - line and space

The ACTIS A150 system achieves significantly higher image resolution than DUV-based mask defect inspection systems. The resolution is high enough to enable state-of-the-art EUV mask pattern recognition and defect detection. Fig. 8 shows test images captured by ACTIS A150. The EUV mask patterns used in this test have half-pitch sizes from 120nm down to 35nm. The images have been processed to reduce background noise. Patterns below 120nm half-pitch were used to test the APMI system and confirm its image quality and resolution capability. A typical example of an EUV mask pattern is a line-and-space pattern with a half-pitch of 60 to 80nm. The image resolution of the ACTIS A150 system is sufficient to make this pattern clearly visible, as seen in the enlarged images for half-pitch 60nm shown in Fig. 9. The pattern size will become smaller when high-NA scanners are introduced for EUV lithography. In the future, mask patterns with a half-pitch of 35nm or so will be used. The image of half-pitch 35nm patterns is shown at the same magnification scale in Fig. 8 and an enlarged image is shown in Fig. 9. Even though the pattern contrast for half-pitch 35nm lines and spaces is lower than that of half-pitch 60nm lines and spaces, the ACTIS A150 system has more than adequate resolution to inspect these patterns. The resolution of the APMI system is adequate to inspect the pattern sizes that are expected to be used when high-NA EUV scanners are introduced.

3.2 Inspection performance of ACTIS A150

The IC industry targets the detection of mask defects that print on a wafer and cause a 10% or larger change in CDs. Defects that cause slightly less than a 10% delta CD by simulation and are detected by an ACTIS A150 inspection are shown in Fig. 10. The left image is an absorber intrusion defect and the right image is an absorber protrusion defect. The contact hole size on the mask is 80nm x 80nm for both patterns. Both defects are less than 20nm on the mask. For the contact hole example shown, simulations shown in Fig. 11 indicate that intrusion and protrusion defects have different degrees of printing impact. Absorber protrusion defects cause a larger impact than absorber intrusion defects. To show defects with a similar lithographic impact, we have chosen to display defects where an absorber edge protrusion defect is smaller than an intrusion defect. In this case, the signal intensity of the contact hole at the center is more than 25% larger (intrusion) or smaller (protrusion) than that of the other contact holes. In both images, one can observe that the top edge of the contact hole at the center has a different shape compared with the rest. These results show that the ACTIS A150 system has the capability to image and detect sub-20nm mask defects that will cause less than a 10% wafer CD deviation from an 80nm mask contact hole.

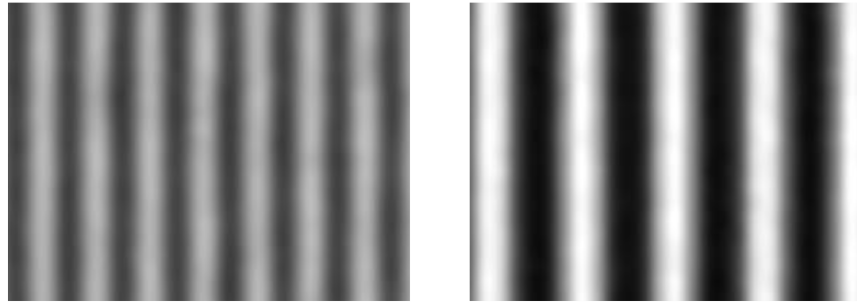


Figure 9. Enlarged images of half-pitch 35nm and 60nm line-and-space pattern. The left image is half-pitch 35nm line and space on mask. The right is half-pitch 60nm line and space.

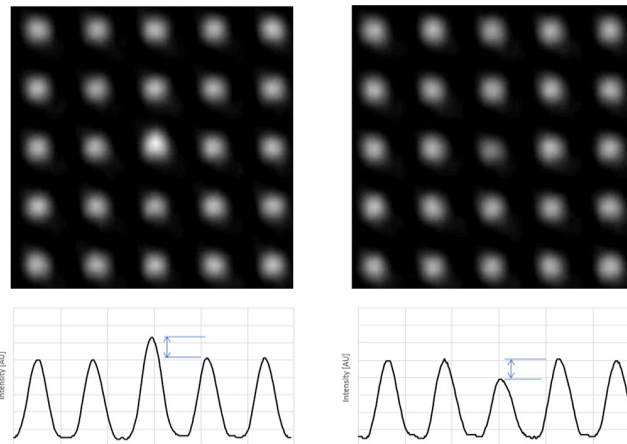


Figure 10. Defect review images of ACTIS A150. The left image contains an absorber intrusion, and the right image contains an absorber protrusion. Both absorber defects are located at the center of the top of the contact. The size of both defects is smaller than 20nm, and their lithographic impact is estimated to be less than a 10% CD deviation. The relative intensity levels are illustrated below the images.

3.3 Image comparison between DUV-based system and actinic inspection system

Fig. 12 is a comparison of images captured by MATRICS, a Lasertec DUV inspection system, and corresponding images from the EUV actinic ACTIS A150. The phase defect of 200nm in width and 2nm in height shown in the first row makes a relatively large impact in lithography. The EUV tool makes it clearly visible whereas the DUV tool does not. The capability to detect phase defects is one of the reasons for using an actinic tool for EUV mask qualification.

The images in the second row show a 32nm CD edge defect in 75nm half-pitch lines and spaces on a mask. The difference of resolution between the two images is readily apparent here. The actinic tool clearly visualizes the CD change deviation in the line-space pattern. On the other hand, the DUV tool neither shows the line-space patterns nor the impact of the defect. The EUV inspection system provides clear and well resolved images to enable the detection of printable defects. Only an EUV actinic inspection system can visualize small surface topology and phase changes that propagate through multilayer stacks.

3.4 Full mask inspection

The capability of the ACTIS A150 system has been tested with multiple full-field masks of various types of device patterns and has been confirmed to be ready for full mask inspection. In full mask inspection, the EUV mask inspection tool detected more defects than conventional DUV inspection tools. It has also been shown that the system can detect not only simple EUV mask defects in patterns such as lines and spaces or contact holes but also more complex defects such as partially-blocked via and missing OPC. The ACTIS A150 provides high resolution images captured by using actinic light and is therefore capable of providing the shape and other properties of the defects it detects. We have confirmed APMI's capability to provide high resolution and high contrast images with EUV illumination.

4. Conclusion

An actinic patterned mask inspection system has been developed to fill a major gap in the EUV mask infrastructure. This is a significant step that facilitates technology advancements in EUV mask fabrication. The APMI system achieves high sensitivity to defects because of its high-resolution imaging capability. It has demonstrated a capability to detect mask defects having an estimated lithographic impact of 10% CD deviation on the wafer. APMI is an ideal method to detect any types of printable defects with EUV lithographic impact. The resolution of the APMI system matches the pattern size expected to be used when high-NA scanners are introduced. Therefore, the current platform is expected to be extended to high-NA EUV.

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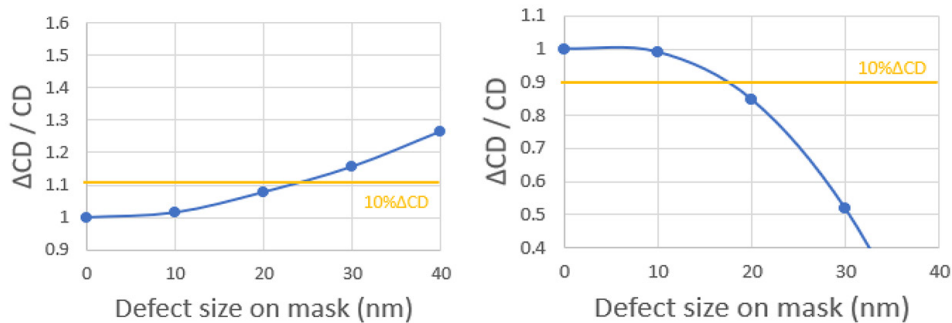


Figure 11. Relation between defect size on mask and CD deviation on wafer. The left graph is simulation for absorber intrusion defect; the right graph is for absorber protrusion defect. Simulation conditions for scanner are projection optics NA 0.33, annular illumination with sigma range between 0.2 to 0.9 and CH pattern size 80nm.

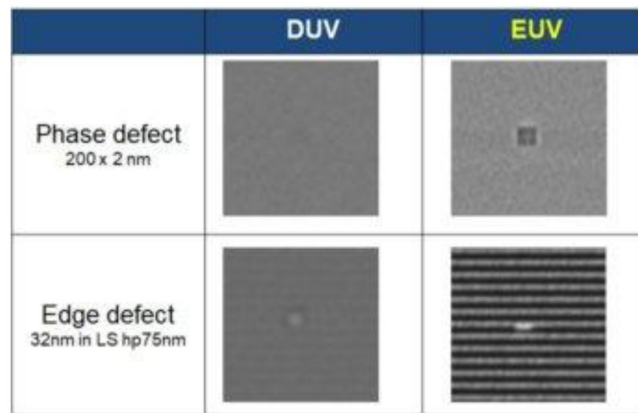


Figure 12. Image comparison between DUV inspection tool and EUV inspection tool. The first-row images contain a phase defect (200nm x 2nm) and the second-row images contain an edge defect (32nm) in a line-and-space-pattern of half-pitch 75nm on the mask.

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Industry Briefs

■ Where Technology Breakthroughs Are Needed

By **Mark LaPedus**

23 December 2019

After years of delays, extreme ultraviolet (EUV) lithography is finally in production at the 7nm logic node with 5nm in the works. But EUV doesn't solve every problem, nor does it address all challenges in the semiconductor industry. The biggest question is still the interconnect, resistance and capacitance. The industry needs new ways to continue 2D scaling, ultra low-power chip architectures, and better next-generation memories and microLEDs. Advanced packaging is still expensive and processes are slow. The integration of chiplets also presents some issues. Emerging memory is not expected to impede significantly on existing NAND or DRAM markets over the next 5-10 years as standalone products. A DRAM replacement will require new architectures and materials. GaN and SiC power semis have made inroads, but both face several challenges. "Despite the early stage success of SiC, there are still many technical challenges that need to be addressed for the technology to reach its full market potential."

The industry needs breakthroughs in other apps, such as AI, autonomous driving, batteries, displays, quantum computing and others. Then, on the fab tool side, chipmakers want new selective deposition equipment, inspection/metrology systems and other gear.

<https://semiengineering.com/what-euv-cant-solve/>

■ Broadcom Bets on Rebound in Core Semiconductor Business

24 December 2019

Broadcom said it plans to separate its wireless business—which sells chips that integrate Bluetooth, WiFi and GPS, and radio frequency chips, from its core semiconductor unit. Broadcom, dented and dinged by the trade war between the United States and China, said in September that its chip business had bottomed out. But after it reported results for the fourth quarter of 2019, the San Diego, California-based company said it should start to rebound in the second half of 2020. Broadcom said its semiconductor segment will add almost 10% in 2020.

Broadcom said it believes the core networking chip business should surge more than 5% to \$12 billion in 2020 as cloud vendors and other customers struggle to address a deluge of data in data centers. Broadcom said its overall sales of semiconductors, including radio frequency and wireless chips, will grow to \$18 billion in 2020. Broadcom's chip business declined 8% to \$17.4 billion in 2019, accounting for about 80% of its total sales. Broadcom has started selling its Tomahawk 4, which is capable of carrying 25.6 trillion units of data per second (Tbps), or double the speed of other solutions on the market. The chip is based on 7-nanometers and incorporates 512 50G SerDes cores, which doubles the port count of its predecessor, the Tomahawk 3. Broadcom said it is targeted at 100 Gb, 200 Gb and 400 Gb Ethernet switches.

<https://www.electronicdesign.com/industrial-automation/article/21119291/broadcom-bets-on-rebound-in-core-semiconductor-business>

■ TSMC's 5-Nanometer Process on Track for First Half of 2020

By **Samuel K. Moore**

13 December 2019

TSMC's 5-nanometer process is on track for high-volume production during the first half of 2020. Compared with the company's 7-nanometer process, used to make iPhone X processors, N5 leads to devices that are 15 percent faster or 30 percent more power efficient. It produces logic that is 1.84 times as small as the previous process and produces SRAM cells that are only 0.021 square micrometers, the most compact ever reported. N5 is the first TSMC process designed around extreme ultraviolet lithography (EUV). The previous generation was developed first using the established 193-nanometer immersion lithography, and then when EUV was introduced, some of the most difficult to produce features were made with the new technology. With more than 10 EUV layers, N5 is the first new process "in quite a long time" that uses fewer photolithography masks than its predecessor.

<https://spectrum.ieee.org/nanoclast/semiconductors/devices/tsmc-5-nanometer-process>

Join the premier professional organization for mask makers and mask users!

About the BACUS Group

Founded in 1980 by a group of chrome blank users wanting a single voice to interact with suppliers, BACUS has grown to become the largest and most widely known forum for the exchange of technical information of interest to photomask and reticle makers. BACUS joined SPIE in January of 1991 to expand the exchange of information with mask makers around the world.

The group sponsors an informative monthly meeting and newsletter, BACUS News. The BACUS annual Photomask Technology Symposium covers photomask technology, photomask processes, lithography, materials and resists, phase shift masks, inspection and repair, metrology, and quality and manufacturing management.

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2020



SPIE Advanced Lithography

23-27 February 2020
San Jose Marriott and
San Jose Convention Center
San Jose, California, USA



Photomask Japan

19-20 April 2020
Yokohama, Japan



The 36th European Mask and Lithography Conference, EMLC 2020

22-24 June 2020
Leuven, Belgium



SPIE Photomask Technology + EUV Lithography

20-24 September 2020
Monterey Conference Center and
Monterey Marriott
Monterey, California, USA

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